

Mr. VIVEKANAND SINGH

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PROFILE SUMMARY

Full Name	Vivekanand Singh
Father's Name	Chandi Prasad Singh
Date of Joining	11/12/2017
Department	Applied Science and Humanities
Qualification	B.Tech. (Industrial Production Engineering)-2009 <i>Institute of Engineering and Rural Technology (IERT), Allahabad (U. P.)</i> M. Tech. (Production Engineering)-2012 <i>Indian Institute of Technology (IIT), Delhi</i>

Additional Roles / Responsibilities at Institute / Department Level (Charge at present):

- Time table co-ordinator
- I/C Maintenance
- I/C Workshop lab
- Departmental timetable co-cordinator
- Departmental examination co-ordinator
- Proctor

Member of Professional Bodies: IAENG

Workshop/Seminar/Conference/Short Term Courses/Faculty Development Programmes

(Not less than one week duration):

04 (Attended);

Key Note Address/Expert Lecture Delivered: nil

B. Tech. Projects Supervised nil

M. Tech. Theses Supervised nil

Books Authored nil