

Mr. Yudhishthir Pandey

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PROFILE SUMMARY

Full Name	Yudhishthir Pandey
Father's Name	Shri Ram Mani Pandey
Date of Joining	11/12/2017
Department	Electrical Engineering
Qualification	B. E. (Electrical & Electronics Engineering)-2002 <i>Krishna Institute of Engineering & Technology, Ghaziabad (U. P.)</i> M. Tech. (Control & Instrumentation)-2011 <i>Indian Institute of Technology, New Delhi, Delhi</i> Ph. D. (Power System)-2018 (Submitted) <i>Jamia Millia Islamia (Central University), New Delhi, Delhi</i>

Additional Roles / Responsibilities at Institute / Department Level (Charge at present):

- I/C. Central Library
- Chairperson, Building Work Committee
- Member of Anti-ragging Committee
- Member of Proctorial Board
- I/C. Measurement Lab

Member of Professional Bodies: IEEE

Publications:

Journal Papers	02 (Published);	01 (Accepted);	01 (Communicated)
Conference Papers	02 (Published);	00 (Accepted);	01 (Communicated)

Workshop/Seminar/Conference/Short Term Courses/Faculty Development Programmes

(not less than one week duration):

03 (Attended);	01 (Organized)
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Key Note Address/Expert Lecture Delivered: 00

B. Tech. Projects Supervised 10 (Completed); 02 (In Progress)

M. Tech. Theses Supervised 01 (Completed); 00 (In Progress)

Books Authored 01(Published);